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**NAND flash memory** forms the core of the removable USB interface storage devices **....** most microprocessors can use NOR **flash memory** as **execute in place** (XIP) **...**
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The low-level interface to **flash memory** chips usually differs from those of other **... NAND flash** memories cannot provide **execute in place** due to their **...**
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It then loads the remainder of the boot code from the **NAND flash memory** into the system's RAM and continues the standard boot process. **...**
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Boot monitor v1.1 does include support for **disk-on-chip** Flash. It can be loaded in the **XIP** block and. can retarget the file I/O library to **disk-on-chip ...**
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2.2.7 **Disk On Chip** (DOC). The DiskOnChip technology was developed by m-systems **.....** where each cell is addressed through negated OR logic and supports **XIP ...**
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